

J1000 U.S. PTO
10/040804



PATENT NUMBER and
ISSUE DATE

Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10040804	11/08/2001	174	2	2641	

**APPLICANTS: Yasuda Masao; Sumikawa Masato;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2000-339954 (P) 11/08/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

56677 (70551)

TITLE : Electronic component and method and structure for mounting semiconductor device

U.S. DEPT. OF COMMERCE PAT. & TM. PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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